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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Daniel G. Borkowski et al.

Title: CYCLE-EFFICIENT TDM TIMESLOT REMAPPING FOR NETWORK PROCESSING  
ENGINES

Docket No.: Intel-007PUS

Serial No.: 10/723,804

Filed: November 26, 2003

Due Date: N/A

Examiner: Hailu, Kimbrom T.

Group Art Unit: 2616

**MS Issue Fee**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

☒ Return postcard and transmittal document.

☒ An Amendment Under 37 C.F.R. §1.312 (13 Pages).

Customer Number: 45780

By

Anthony T. Moosey

Reg. No. 55,773

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 18th day of September, 2007.

Shellie Bailey

(GENERAL)



Attorney's Docket No: Intel-007PUS  
Intel Docket Number P17383

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Title : CYCLE-EFFICIENT TDM TIMESLOT REMAPPING FOR NETWORK  
PROCESSING ENGINES

**MAIL STOP ISSUE FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

37 C.F.R. § 1.312 AMENDMENT

Please amend the above-identified application as follows: